

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10087416	02/27/2002	257		2811	OWENS

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JCH/958

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF 09/444,024 11/19/1999
WHICH IS A DIV OF 09/386,076 08/30/1999 PAT 6,093,661

** FOREIGN APPLICATIONS VERIFIED:

NOTE: DISCLAIMER:
the term of this patent shall not extend
beyond the expiration date
of Pat. No. 6,417,546

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO MI22-1965
TITLE : Integrated circuitry and semiconductor processing method of forming field effect transistors <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
X TERMINAL DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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